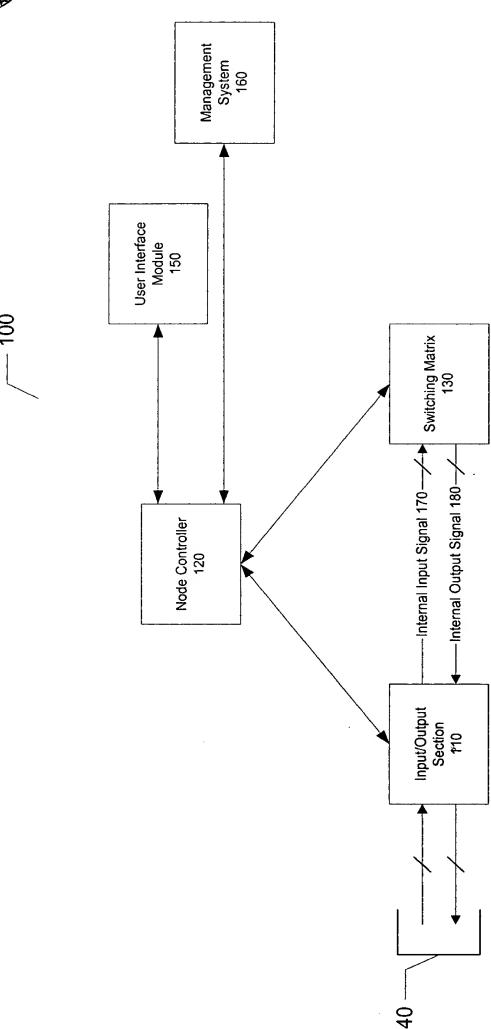
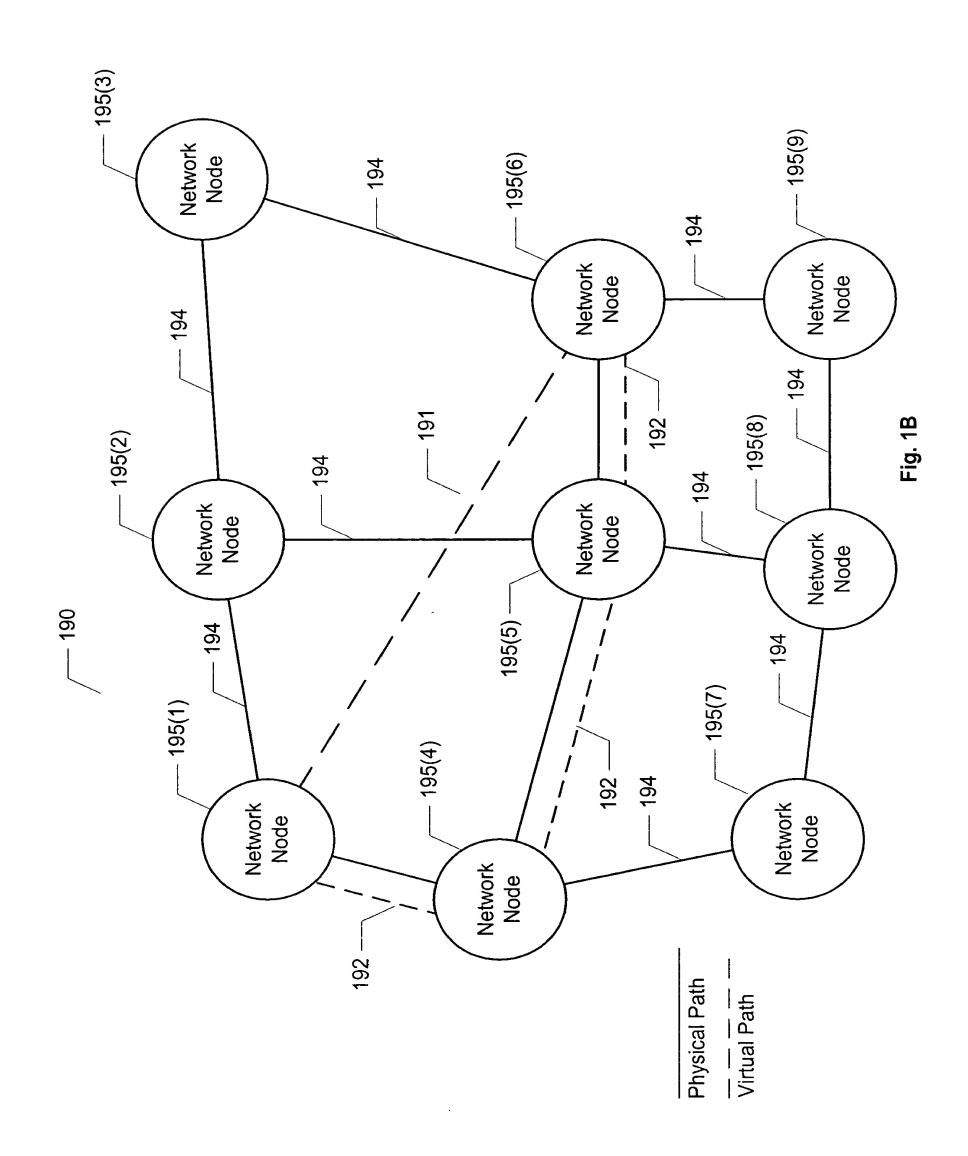
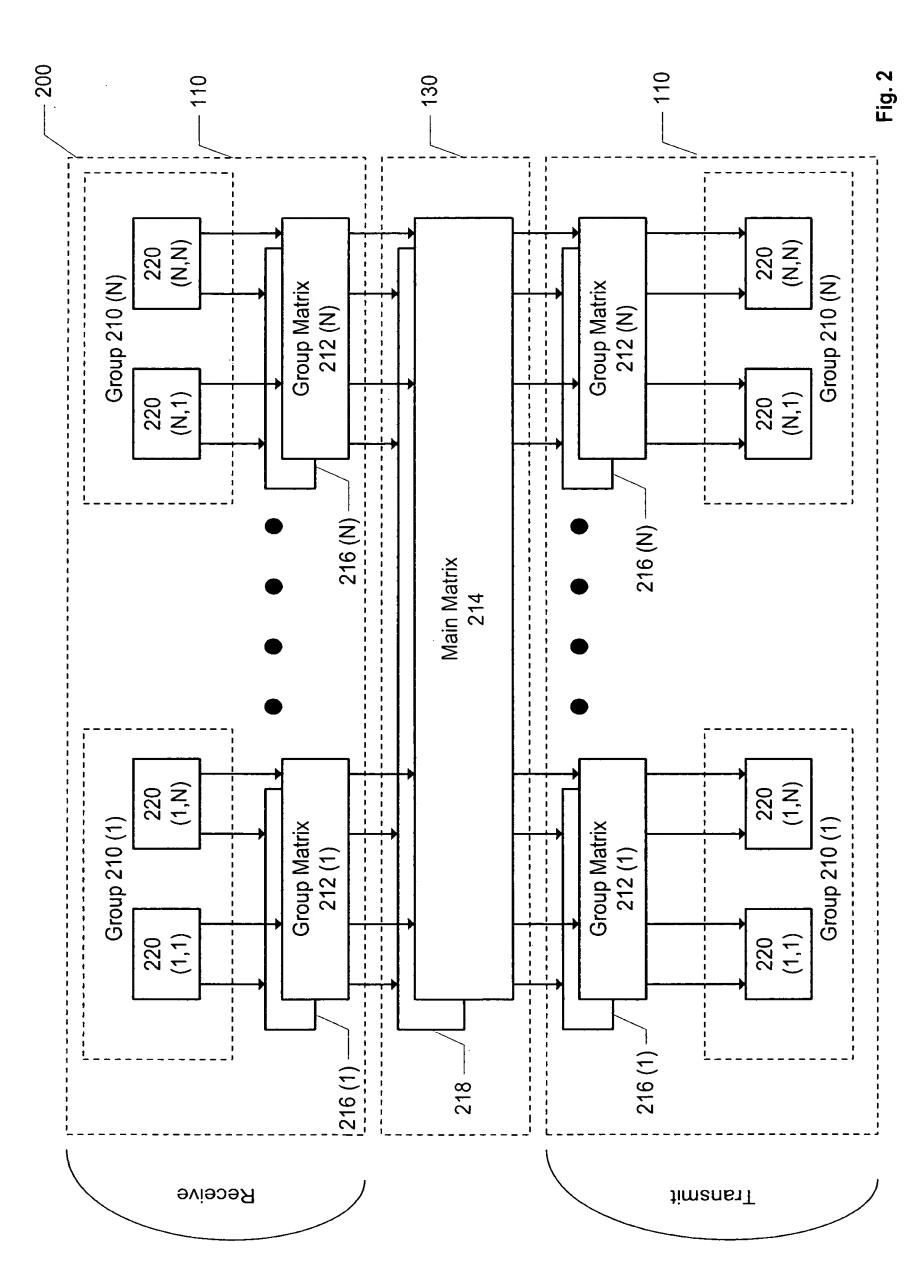
·1/20 '

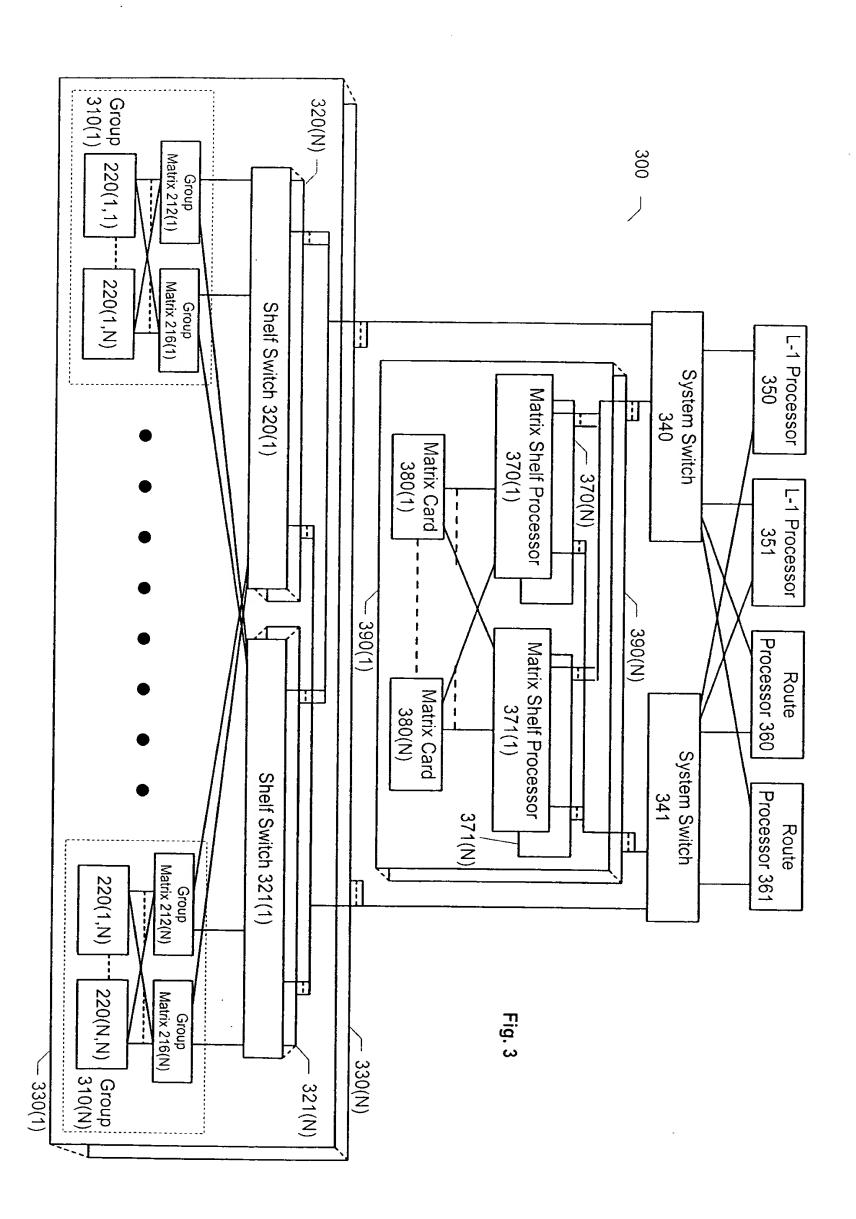




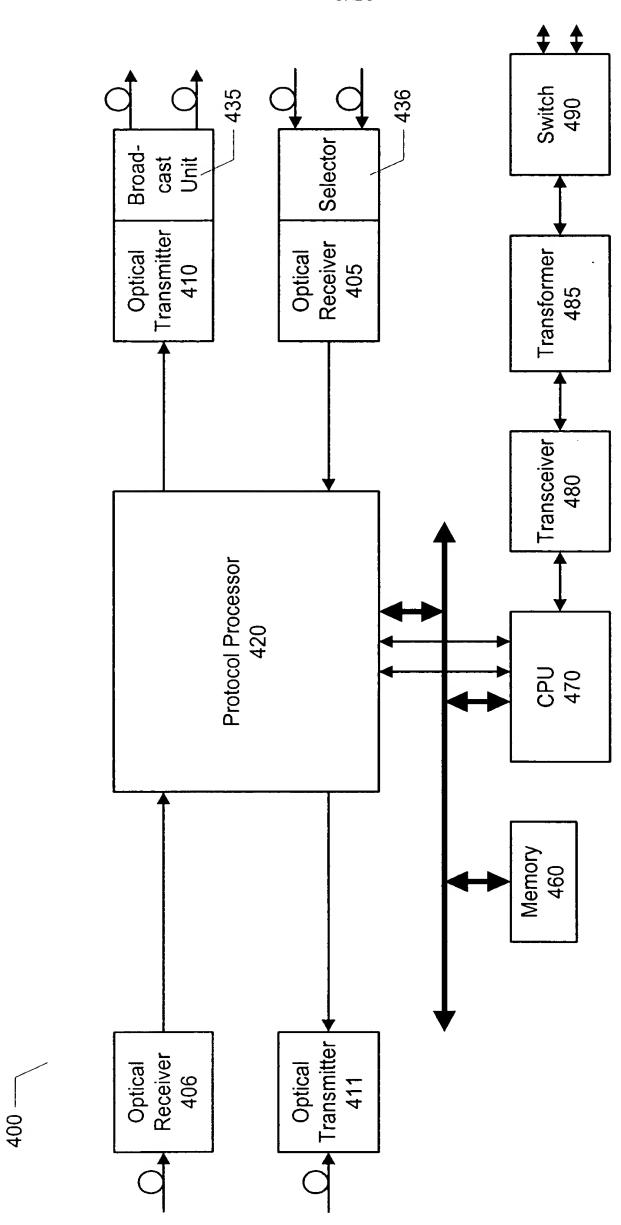
·2/20



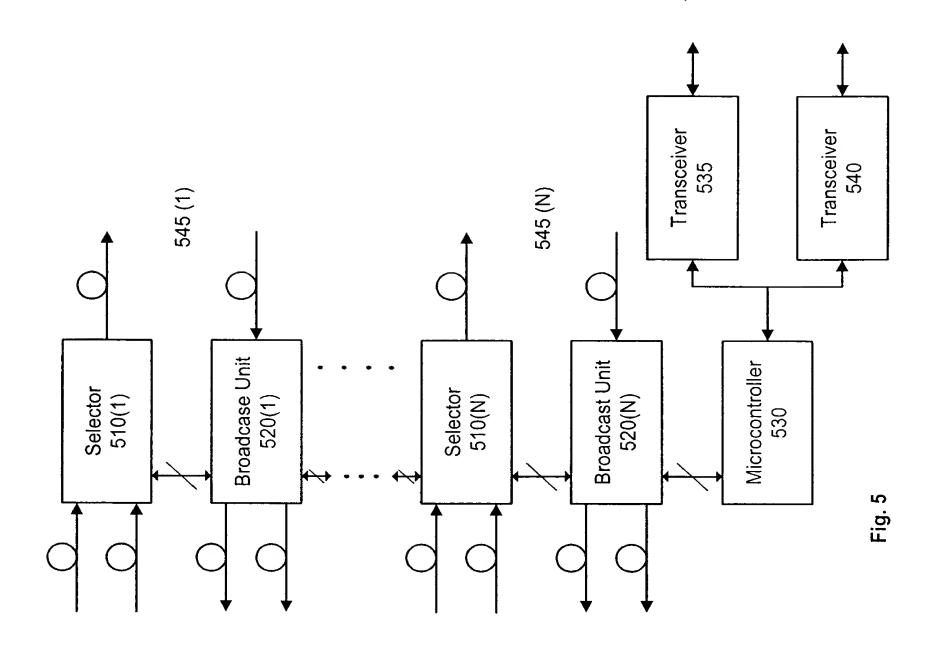




·5/20

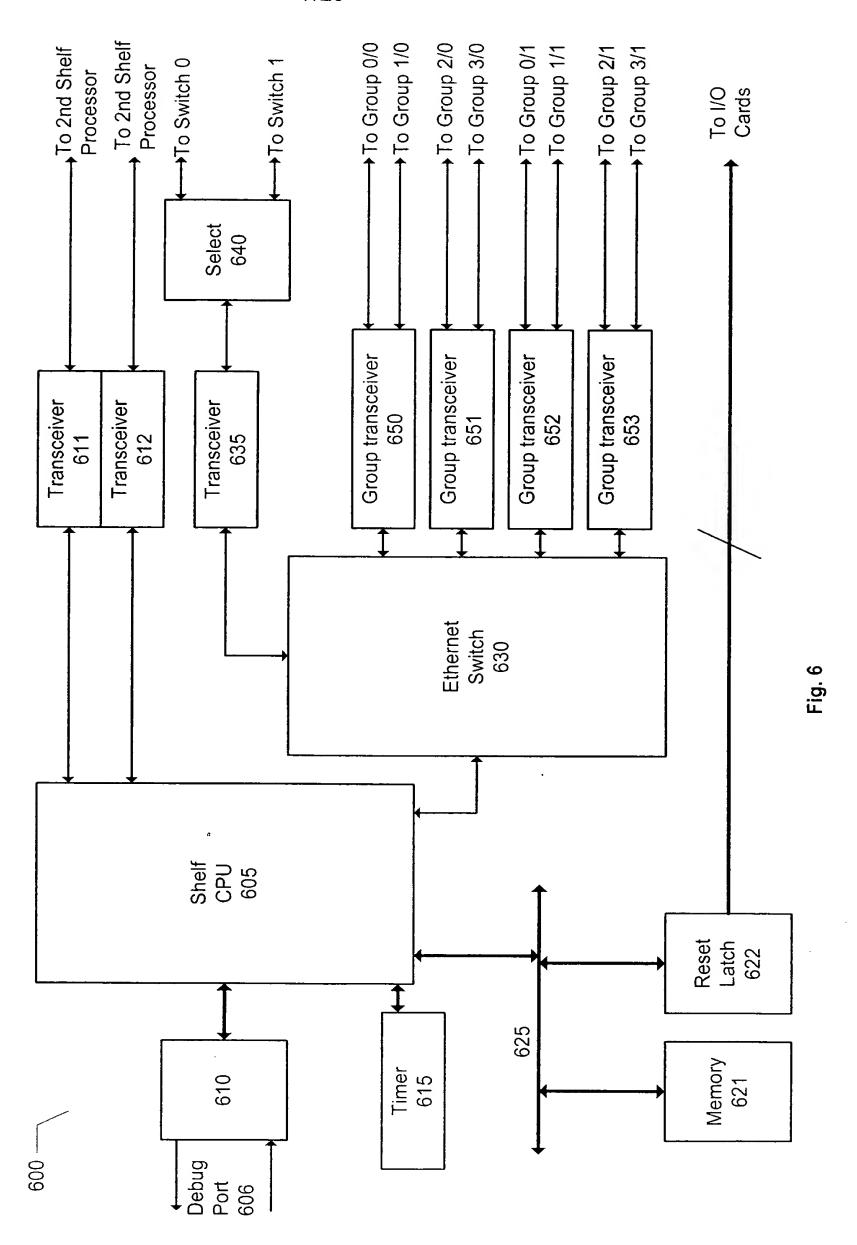


6/20

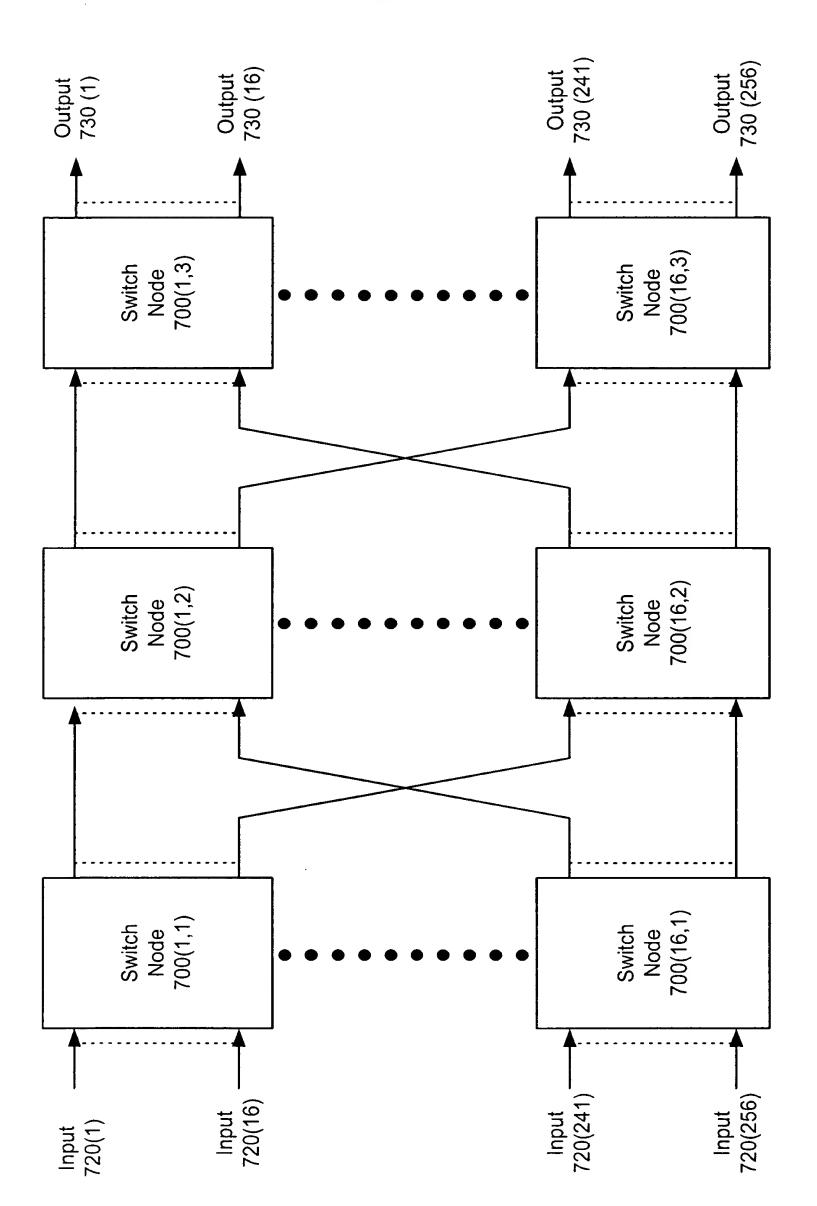


200

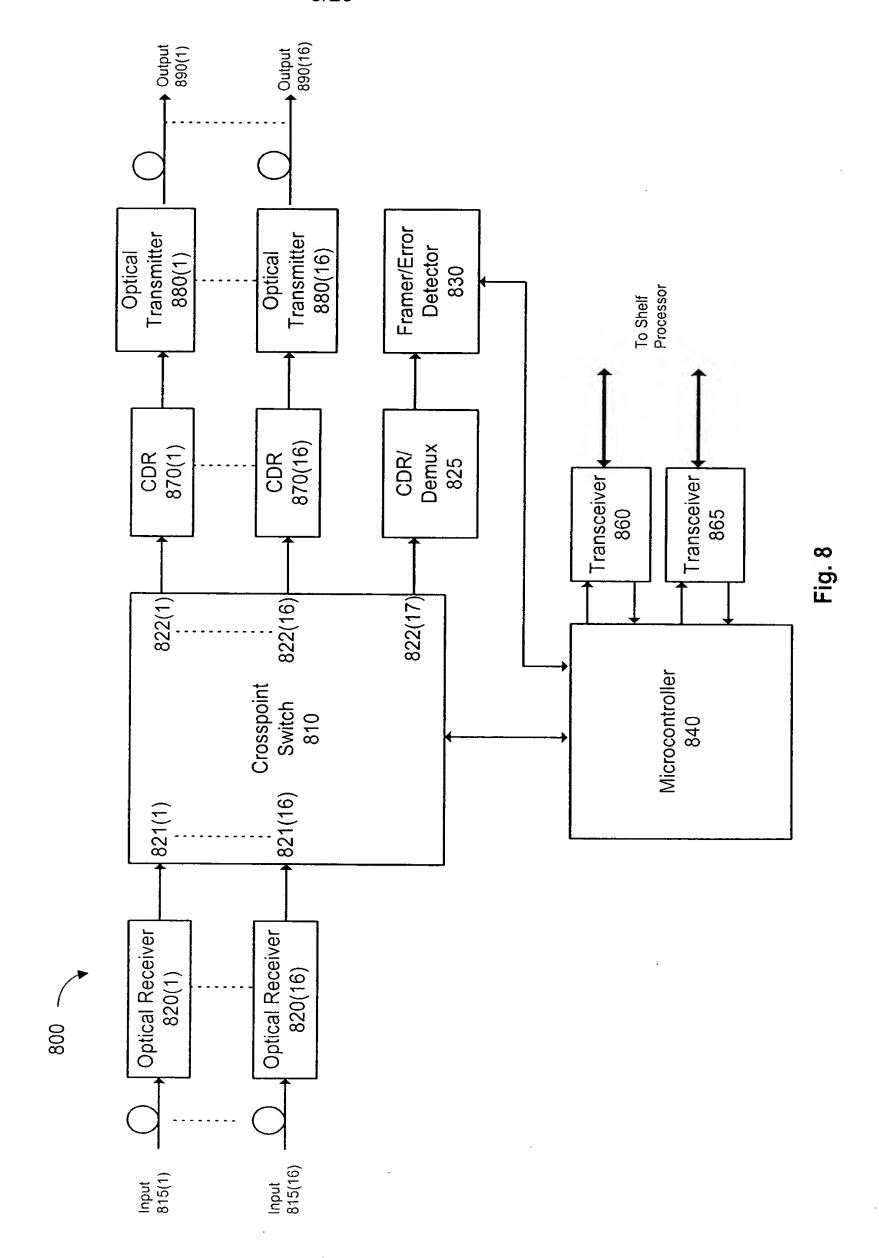
7/20

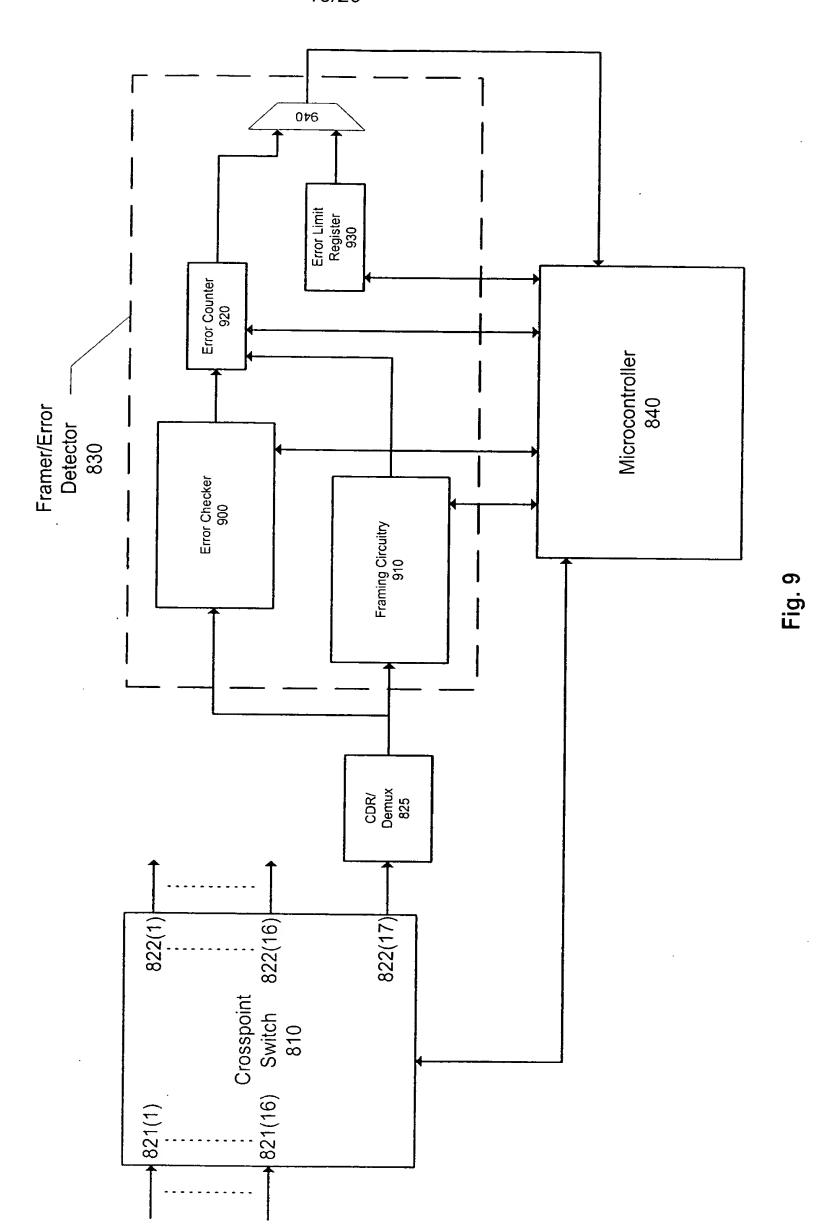


Title: Method And Apparatus For Isolating Faults In A Switching Matrix



Fia. 7





Title: Method And Apparatus For Isolating Faults In A Switching Matrix

11/20 '

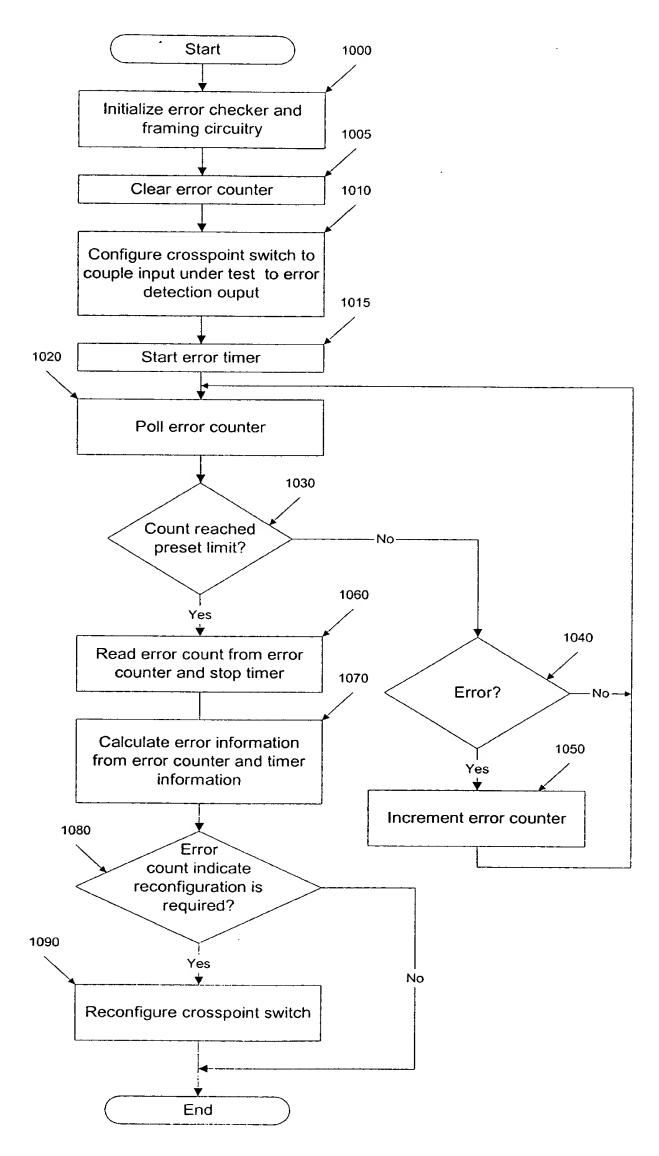
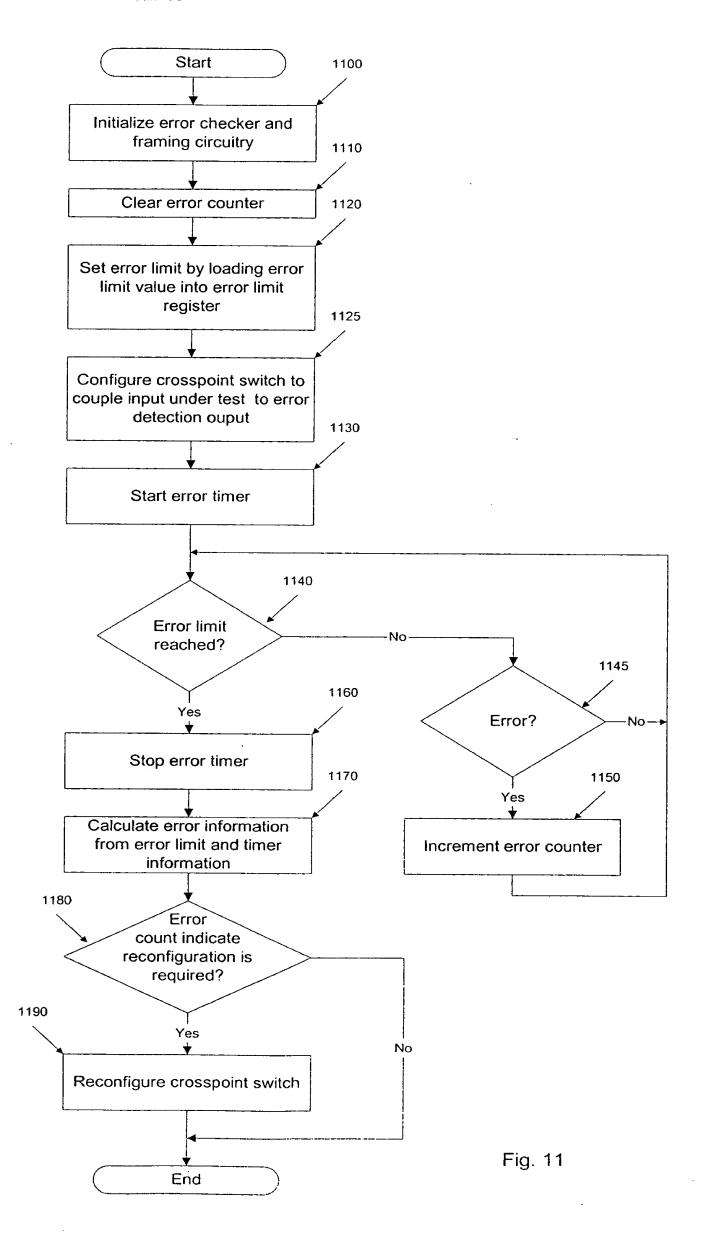
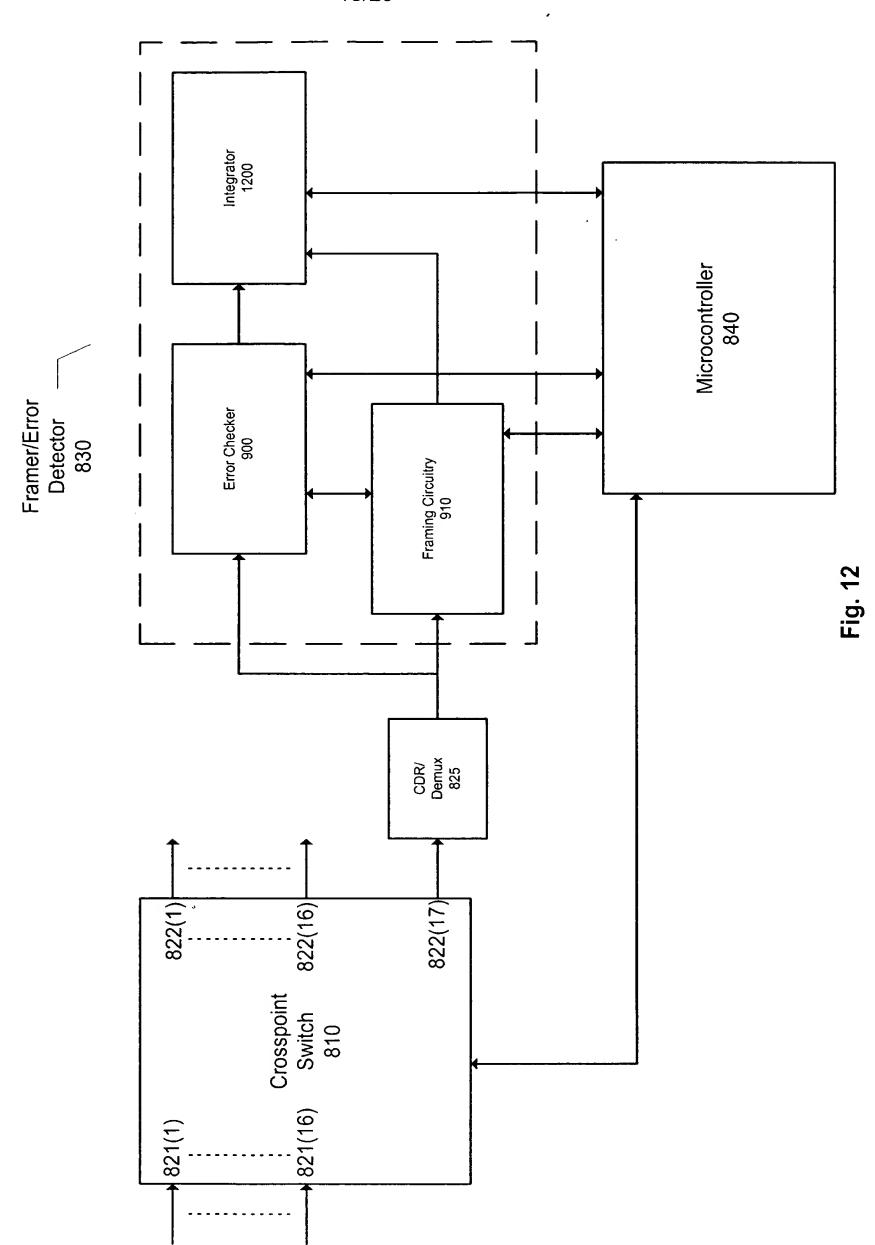


Fig. 10

Title: Method And Apparatus For Isolating Faults In A Switching Matrix

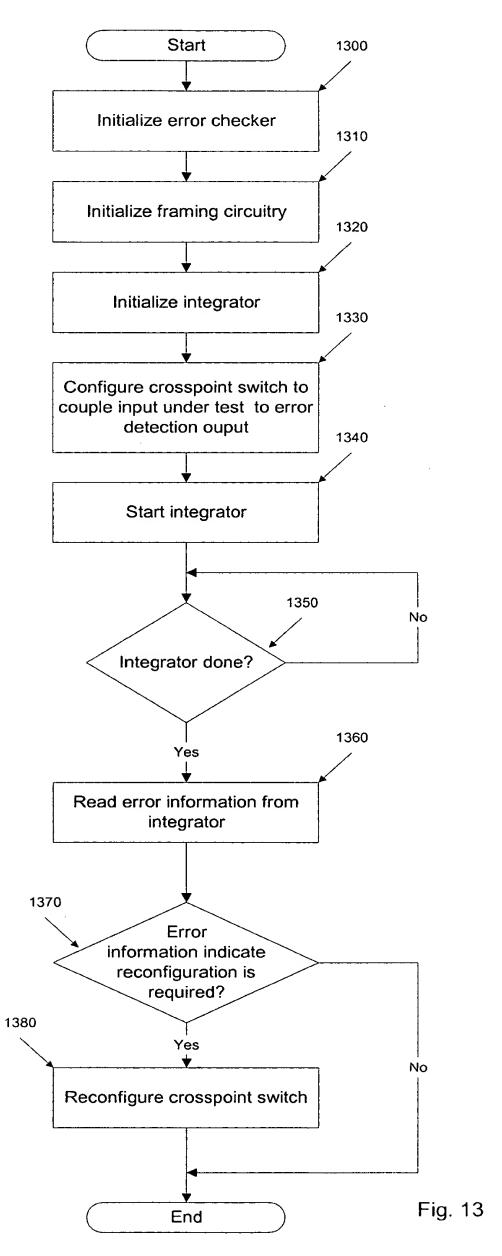


13/20:



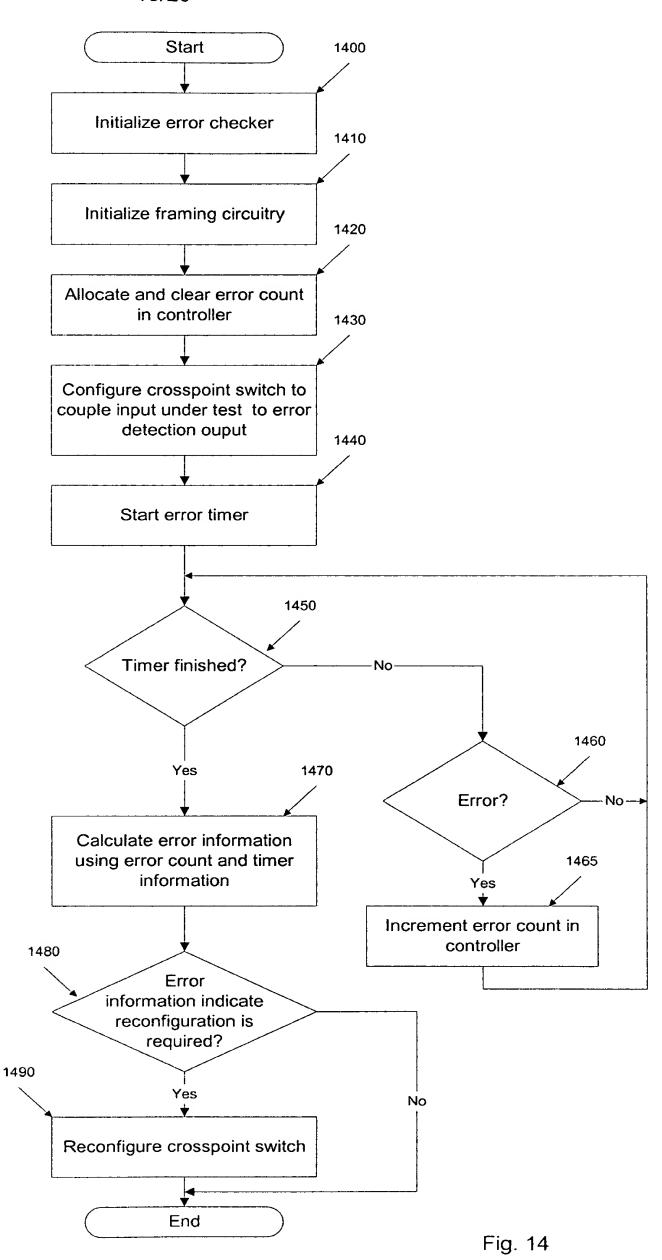
Title: Method And Apparatus For Isolating Faults In A Switching Matrix

14/20

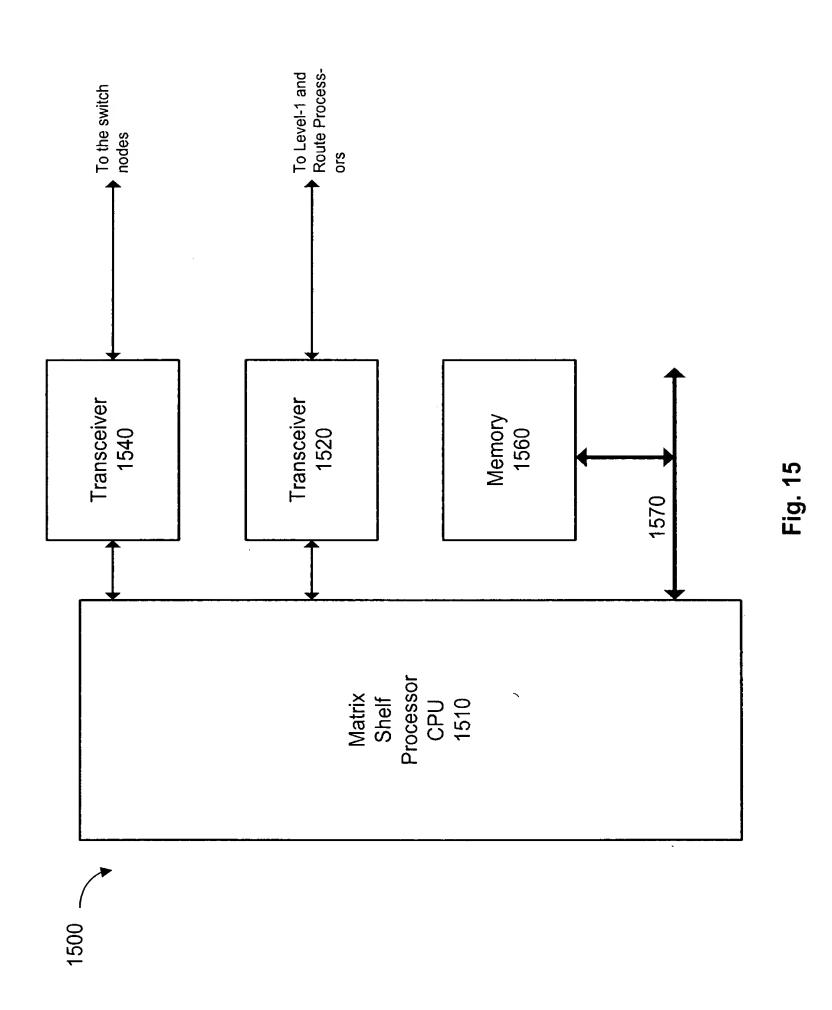


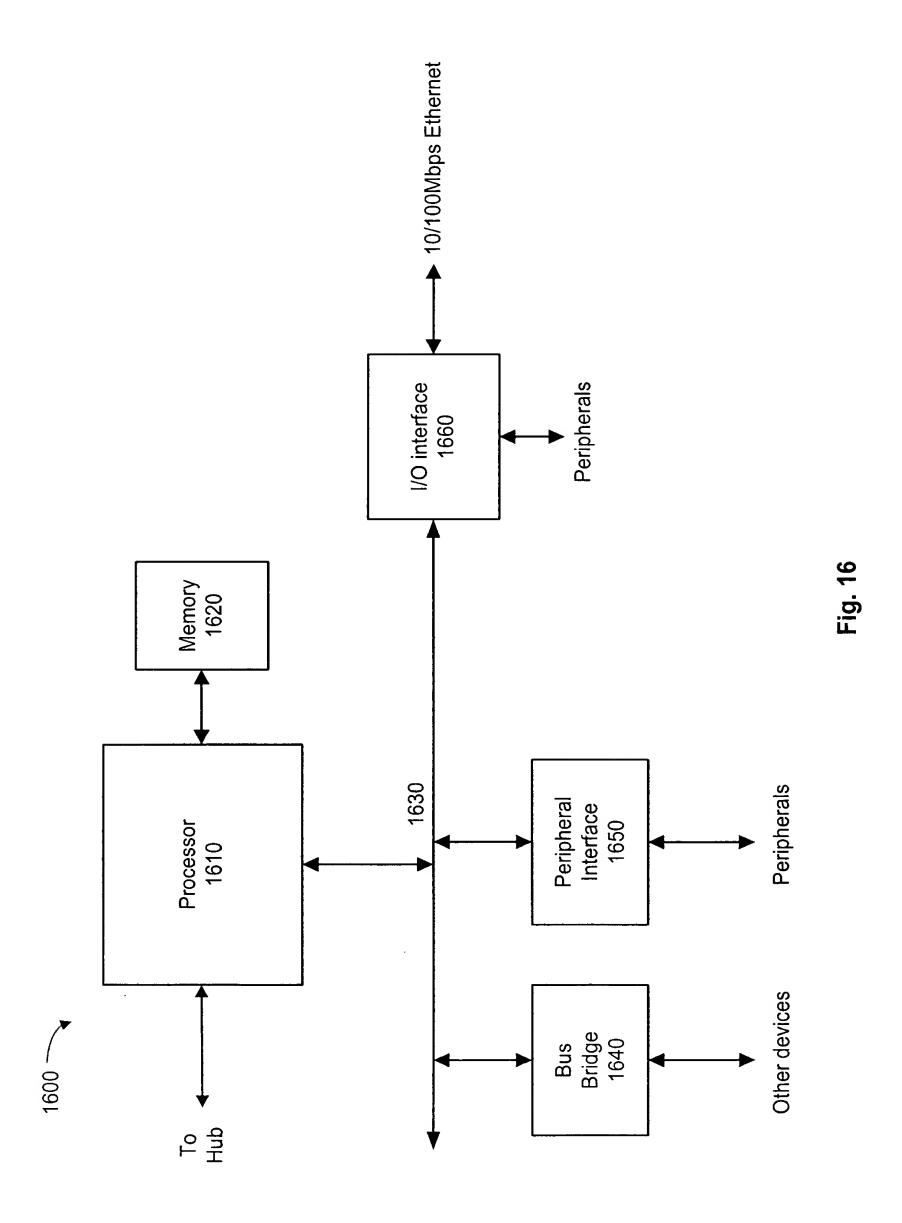
Title: Method And Apparatus For Isolating Faults In A Switching Matrix

15/20 .

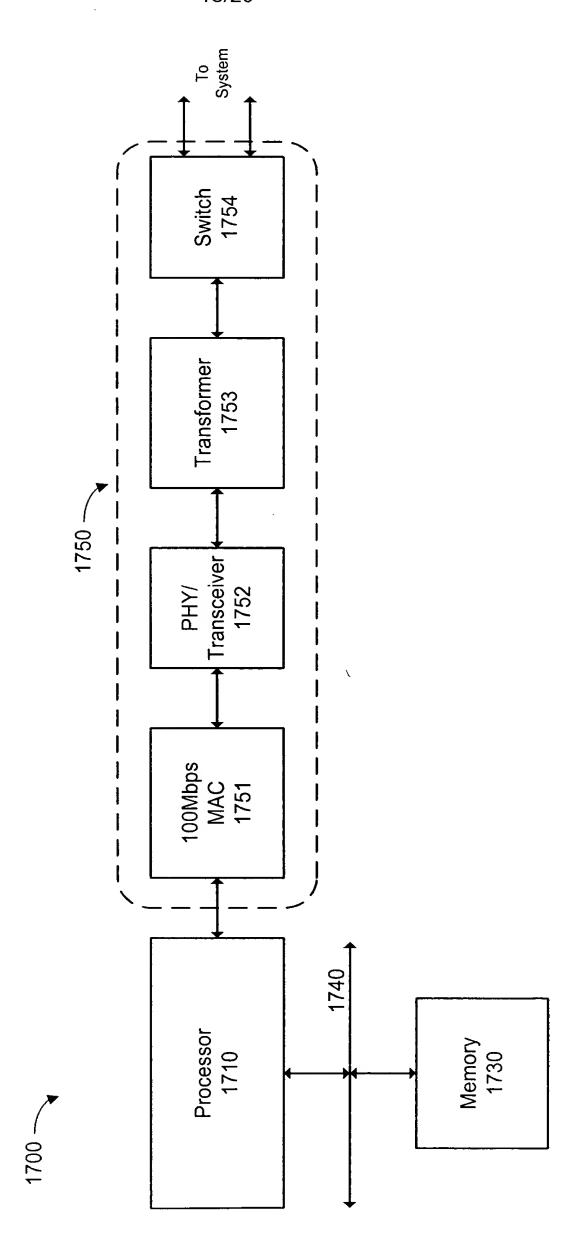


16/20 *****





48/20 -



Application Serial No.: 09/477,217

First Named Inventor: Ronald Alan Russell
Title: Method And Apparatus For Isolating Faults In A Switching Matrix

19/20 -

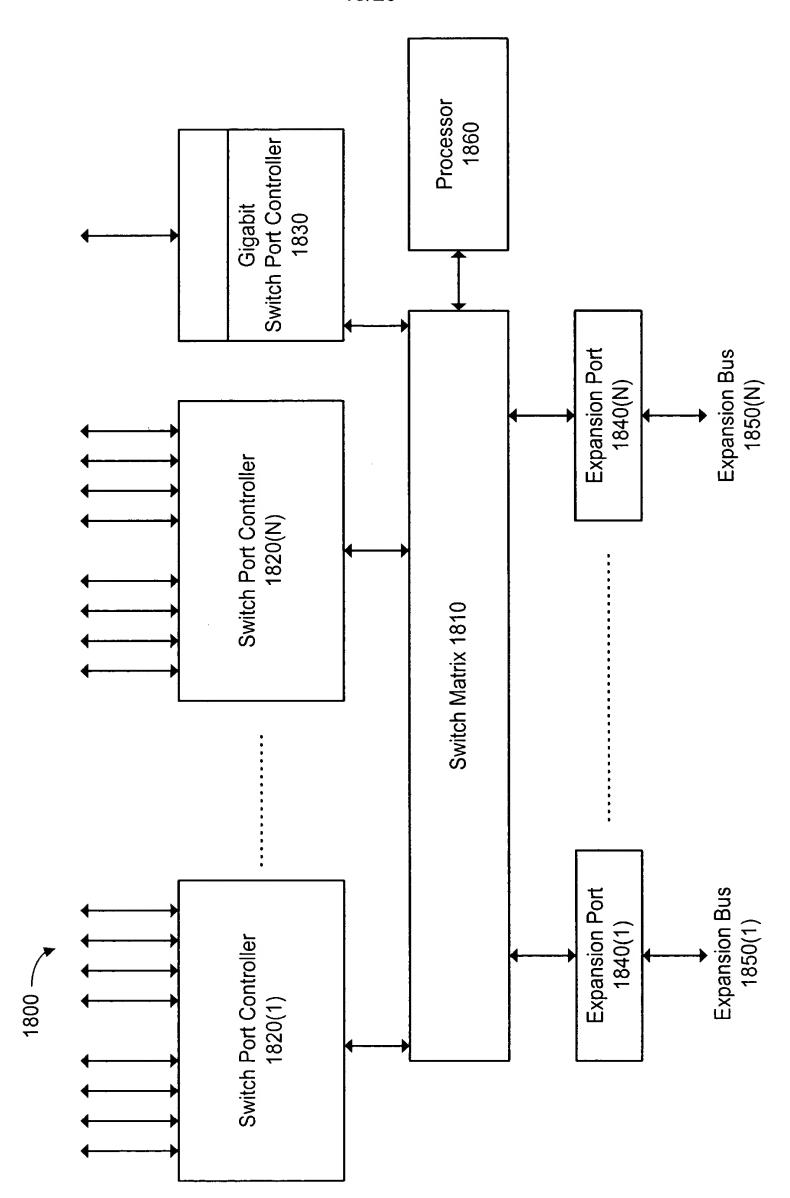


Fig. 18

Title: Method And Apparatus For Isolating Faults In A Switching Matrix

20/20 *

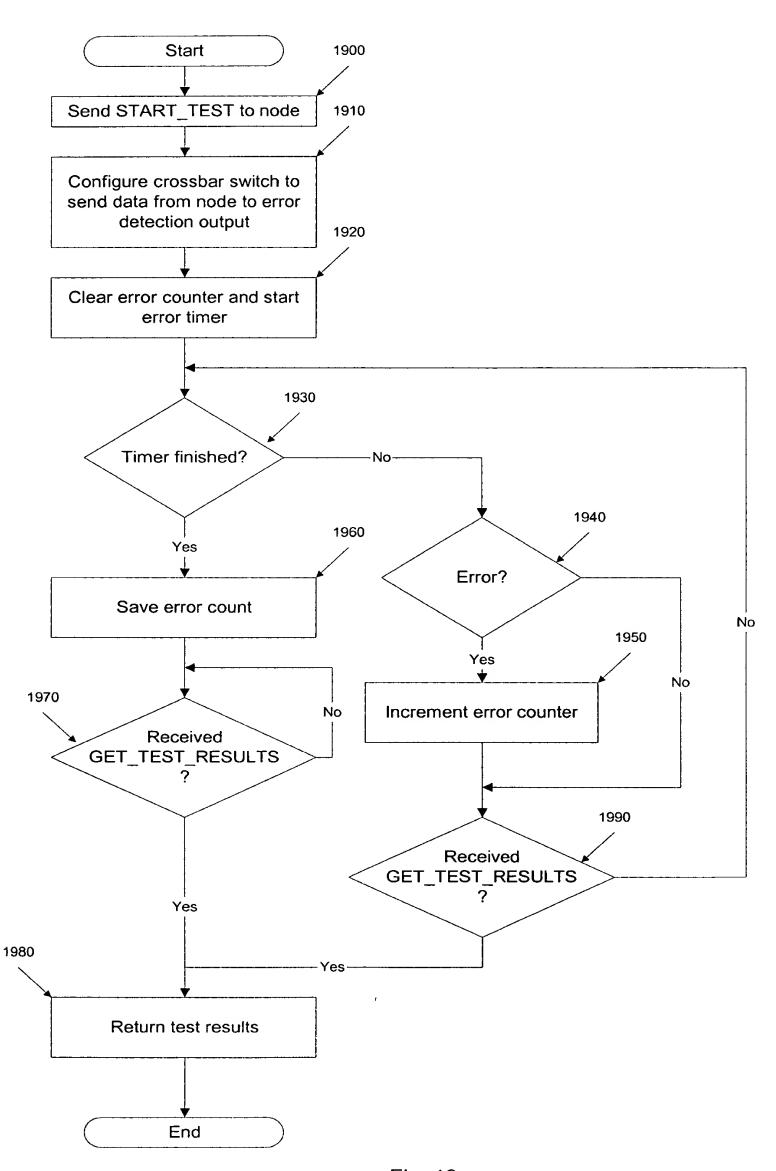


Fig. 19